

IMB-1232-WV

User Manual

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- (2) this device must accept any interference received, including interference that may cause undesired operation.

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WARNING

THIS PRODUCT CONTAINS A BUTTON BATTERY If swallowed, a button battery can cause serious injury or death. Please keep batteries out of sight or reach of children.

CALIFORNIA, USA ONLY

The Lithium battery adopted on this motherboard contains Perchlorate, a toxic substance controlled in Perchlorate Best Management Practices (BMP) regulations passed by the California Legislature. When you discard the Lithium battery in California, USA, please follow the related regulations in advance.

"Perchlorate Material-special handling may apply, see <u>www.dtsc.ca.gov/hazardouswaste/</u> <u>perchlorate</u>"

AUSTRALIA ONLY

Our goods come with guarantees that cannot be excluded under the Australian Consumer Law. You are entitled to a replacement or refund for a major failure and compensation for any other reasonably foreseeable loss or damage caused by our goods. You are also entitled to have the goods repaired or replaced if the goods fail to be of acceptable quality and the failure does not amount to a major failure. If you require assistance please call ASRockInd Tel : +886-2-28965588 ext.123 (Standard International call charges apply)

CE

ASRockInd follows the green design concept to design and manufacture our products, and makes sure that each stage of the product life cycle of ASRockInd product is in line with global environmental regulations. In addition, ASRockInd disclose the relevant information based on regulation requirements.



DO NOT throw the motherboard in municipal waste. This product has been designed to enable proper reuse of parts and recycling. This symbol of the crossed out wheeled bin indicates that the product (electrical and electronic equipment) should not be placed in municipal waste. Check local regulations for disposal of electronic products.

Button Battery Safety Notice

- INGESTION HAZARD: This product contains a button cell or coin battery.
- DEATH or serious injury can occur if ingested.
- A swallowed button cell or coin battery can cause Internal Chemical Burns in as little as 2 hours.
- KEEP new and used batteries OUT OF REACH of CHILDREN
- Seek immediate medical attention if a battery is suspected to be swallowed or inserted inside any part of the body.
- Remove and immediately recycle or dispose of used batteries according to local regulations and keep away from children. Do NOT dispose of batteries in household trash or incinerate.
- Even used batteries may cause severe injury or death.
- Call a local poison control center for treatment information.
- Battery type: CR2032
- Battery voltage: 3V
- Non-rechargeable batteries are not to be recharged.
- Do not force discharge, recharge, disassemble, heat above (manufacturer's specified temperature rating) or incinerate. Doing so may result in injury due to venting, leakage or explosion resulting in chemical burns.
- This product contains an irreplaceable battery.
- This icon indicates that a swallowed button battery can cause serious injury or death. Please keep batteries out of sight or reach of children.



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Chapter 1: Introduction

Thank you for purchasing ASRockInd *IMB-1232-WV* motherboard, a reliable motherboard produced under ASRockInd's consistently stringent quality control. It delivers excellent performance with robust design conforming to ASRockInd's commitment to quality and endurance.

In this manual, chapter 1 and 2 contain introduction of the motherboard and stepby-step guide to the hardware installation. Chapter 3 and 4 contain the configuration guide to BIOS setup and software support.



Because the motherboard specifications and the BIOS software might be updated, the content of this manual will be subject to change without notice. In case any modifications of this manual occur, the updated version will be available on ASRockInd website without further notice. You may find the latest CPU support lists on ASRockInd website as well. ASRockInd website: <u>https://www.asrockind.com/IMB-1232-WV</u> If you require technical support related to this motherboard, please visit our website for specific information about the model you are using. <u>https://www.asrockind.com/technical-support</u>

1.1 Package Contents

ASRockInd *IMB-1232-WV* Motherboard (Mini-ITX (6.7-in x 6.7-in)) 1 x I/O Panel Shield

1.2 Specifications

Form	Dimensions	Mini-ITX (6.7-in x 6.7-in x 1.0-in, 17.0 cm x 17.0	
Factor	Dimensions	cm x 2.5 cm)	
		Intel [®] 14th/13th/12th Gen (Raptor Lake-S	
	CPU	Refresh/Raptor Lake-S/Alder Lake-S) Core™	
Processor		Processors, up to 65W	
System	Chipset	Intel [®] H610	
	Socket	LGA1700	
	BIOS	AMI SPI 256 Mbit	
	Technology	Dual Channel DDR4 3200 MHz	
Memory	Capacity	64GB (32 GB per DIMM)	
	Socket	2 x 260-pin SO-DIMM	
	Controller	Intel [®] UHD Graphics	
	DisplayPort	DisplayPort 1.4a, DP++	
	DisplayPort	Max resolution up to 4096x2160@60Hz	
	HDMI	HDMI 2.0b	
Graphics		Max resolution up to 4096x2160@60Hz	
	eDP	eDP1.4b	
		Max resolution up to 4096x2160@60Hz	
	LVDS	Dual channel 24 bit up to 1920x1200@60Hz	
	MultiDisplay	Triple Display	
	PCle	1 x PCle x16 (Gen4)	
		1 x M.2 (Key E, 2230) with PCIe x1, USB 2.0	
Expansion	M.2	and CNVi for Wireless	
Slot		1 x M.2 (Key B, 3042/3052) with PCIe x1/	
		USB3.2 Gen1/USB 2.0 and SIM for 4G/5G	
	SIM Socket	1 x Socket connected to M.2 Key B	
Audio	Interface	Realtek ALC897, High Definition Audio. Line-	
Addio	Interface	out, Mic-in	
		LAN1: Intel [®] I225LM/I225V with	
Ethernet	Controller/	10/100/1000/2500 Mbps	
	Speed	LAN2: Intel [®] I225LM/I225V with	
		10/100/1000/2500 Mbps	
	Connector	2 x RJ-45	

	Ethernet	2 x 2.5 Gigabit LAN	
	USB	2 x USB 3.2 (Gen2)	
Poar I/O		2 x USB 2.0	
	HDMI	1 x HDMI 2.0b	
	DisplayPort	2 x DP 1.4a++	
	Audio	2 (Mic-in, Line-out)	
		1 x USB 3.2 Gen1 (1 x USB 3.2 header)	
	036	3 x USB 2.0 (1 x 2.54 pitch header)	
	COM	COM1, COM2 (RS-232/422/485)	
		COM3, COM4 (RS-232)	
	GPIO	4 x GPI, 4 x GPO	
Internal	TPM	TPM 2.0 onboard IC	
Connector	LVDS	1	
	eDP	1	
	SATA PWR	1	
	Output		
	Speaker	1	
	Header		
	M.2	1 x M.2 (Key M, 2242/2260/2280) with PCIe	
Storage		Gen3 x4 for SSD	
	SATA	2 x SATA3 (6Gb/s)	
Watchdog	Output	From Super I/O to drag RESETCON#	
Timer	Interval	256 Segments, 0, 1, 2,255sec	
	Input D\//P	12~28V DC-In with 4-pin wafer PWR cable	
		or DC Jack(Screw type)	
Power		AT/ATX Supported	
Requirements	Bower On	- AT: Directly PWR on as power input ready	
		- ATX: Press button to PWR on after power	
		input ready	
	Operating	-20°C ~ 70°C	
	Temp	-20 0 10 0	
	Storage	-40°C ~ 85°C	
Environment	Temp	-40 0 - 63 0	
Environment	Operating	5% ~ 90%	
	Humidity		
	Storage	5% ~ 90%	
	Humidity		

1.3 Motherboard Layout



- 1 : DDR4 SO-DIMM Sockets
- 2 : ESPI Header (ESPI1)
- 3 : M.2 Key-B Socket (M2_B1)
- 4 : Battery Connector
- 5 : SATA Power Output Connector
- 6 : SATA3 Connectors (SATA3_1, SATA3_2)
- 7 : DACC1
- 8 : Digital Input / Output Default Value Setting (JGPIO_SET1)
- 9 : Digital Input / Output Power Select (JGPIO_PWR1)
- 10 : Digital Input/Output Pin Header (JGPIO1)
- 11 : COM Port Headers (COM1, 2, 3, 4)
- 12 : Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)
- 13 : COM Port PWR Setting Jumpers PWR_COM3 (For COM Port3) PWR_COM1 (For COM Port1)
- 14 : Inverter Power Control Wafer (BLT_PWR1)
- 15 : LVDS Panel Connector*
 - * eDP Connector (on the Backside of PCB)
- 16 : Backlight Volume Control (BLT_VOL1)
- 17 : Panel Power Select (LCD_VCC) (PNL_PWR1)
- 18 : Inverter Power Control Wafer (BLT_PWR2)
- 19 : Clear CMOS Header (CLRMOS1)
- 20 : Chassis Intrusion Header (CI1_2)
- 21 : PWR_BAT1_SIO_AT1
- 22 : AT_TEST1_PCIE_PWR1
- 23 : System Panel Header
- 24 : SMBUS_TEST1
- 25 : CPU FAN Connector (+12V)
- 26 : Chassis FAN Connector (+12V)
- 27 : Power Adapter
- 28 : M.2 Key-M Socket (M2_KEYM1)
- 29 : M.2 Key-E Socket (M2_KEYE1)
- 30 : 4-pin ATX PWR Connector
- 31: 2-pin UPS Module Power Input Connector
- 32 : SPDIF Header
- 33 : Front Panel Audio Header
- 34 : 3W Audio AMP Output Wafer
- 35 : USB2.0 Header (USB2_5_6)
- 36 : USB3.2 Gen1 Header (USB3_4_9)

Back Side :

SIM Card Socket (SIM1)

eDP Connector (EDP1, refer to No. 15)

1.4 I/O Panel



* There are two LED next to the LAN port. Please refer to the table below for the LAN port LED indications.



Chapter 2: Installation

This is a Mini-ITX form factor $(6.7" \times 6.7")$ motherboard. Before you install the motherboard, study the configuration of your chassis to ensure that the motherboard fits into it.



Make sure to unplug the power cord before installing or removing the motherboard. Failure to do so may cause physical injuries to you and damages to motherboard components.



ASRock Industrial has positioned the chipset on the rear of the PCB to optimize space and improve thermal dissipation when the chipset heatsink contacts the chassis via a thermal pad. Ensure the height of the chipset heatsink is considered during system integration.

2.1 Screw Holes

Place screws into the holes to secure the motherboard to the chassis.



Do not over-tighten the screws! Doing so may damage the motherboard.

2.2 Pre-installation Precautions

Take note of the following precautions before you install motherboard components or change any motherboard settings.

- 1. Unplug the power cord from the wall socket before touching any component.
- To avoid damaging the motherboard components due to static electricity, NEVER place your motherboard directly on the carpet or the like. Also remember to use a grounded wrist strap or touch a safety grounded object before you handle components.
- 3. Hold components by the edges and do not touch the ICs.
- 4. Whenever you uninstall any component, place it on a grounded antistatic pad or in the bag that comes with the component.



Before you install or remove any component, ensure that the power is switched off or the power cord is detached from the power supply. Failure to do so may cause severe damage to the motherboard, peripherals, and/or components.

2.3 Installation of Memory Modules (SO-DIMM)

This motherboard provides two 260-pin DDR4 (Double Data Rate 4) SO-DIMM slots.

Step 1. Align a SO-DIMM on the slot such that the notch on the SO-DIMM matches the break on the slot.





- The SO-DIMM only fits in one correct orientation. It will cause permanent damage to the motherboard and the SO-DIMM if you force the SO-DIMM into the slot at incorrect orientation.
- 2. Please do not intermix different voltage SO-DIMMs on this motherboard.
- Step 2. Firmly insert the SO-DIMM into the slot until the retaining clips at both ends fully snap back in place and the SO-DIMM is properly seated.

2.4 Expansion Slots

There is 1 PCI Express slot, 4 M.2 sockets and 1 SIM socket on this motherboard.

PCIE slot: PCIE1 (PCIE 4.0 x16 slot) is used for PCI Express x16 lane width cards.

Due to power design of the motherboard, we recommend customer using the power adapter with suggested DC-input voltage shown in the table below for system stability.

PCIE Add-on card (Power consumption)	Suggested DC-input voltage
N/A	12V~28V
75W or lower	19V~28V
Higher than 75W*	24V~28V

* The VGA-PWR card (*Optional*) is required to support additional +12V input power for PCIE Add-on card.

M.2 sockets:

1 x M.2 (Key E, 2230) with PCIe x1, USB 2.0 and CNVi for Wireless. 1 x M.2 (Key B, 3042/3052) with PCIe x1/USB3.2 Gen1/USB 2.0 and SIM for 4G/5G.

1 x M.2 (Key M, 2242/2260/2280) with PCIe Gen3 x4 for SSD.

M.2 Key-E Socket (M2 KEYE1)

Pin	Signal Name	Signal Name	Pin
1	GND	+3.3V	2
3	USB_D+	+3.3V	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_ CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BGI_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST0#	52
55	WAKE#	W DISABLE1#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	NA	58
61	CNV_WT_D1+	NA	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_ LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3.3V	72
75	GND	+3.3V	74

M.2 Key-M Socket (M2_KEYM1)

PI Signal Name Signal Name Signal Name Signal Name Pi 0 0-00 +3.5.V 2 - <				
1 GND +3.3V 2 1 GND +3.3V 4 5 GRD +3.4V 4 5 GRD +3.4V 4 5 GRD NA 6 5 GRD SATA 10 11 PETp3 +3.3V 12 15 ORD +3.3V 14 15 FEfra2 +3.3V 16 16 FEfra2 NA 20 17 PERo2 NA 20 18 PETp2 NA 20 19 PETp2 NA 20 20 PETp2 NA 20 21 PETp2 NA 20 21 PETp2 NA 20 21 PETp2 NA 20 22 FETp1 NA 30 23 PETP12 NA 40 33 PETP1 NA 42	Pin	Signal Name	Signal Name	Pin
3 OKD +3.3V 4 3 OKD +3.3V 4 7 PERS N.M. 6 7 PERS -3.3V 12 15 OKD -3.3V. 12 15 OKD -3.3V. 14 17 PERS +3.3V. 14 17 OKD -3.3V. 16 17 PERS +3.3V. 16 17 PERS -3.3V. 16 17 OKD NA 32 18 PERS -4.3.3V. 16 17 PERS -4.3.3V. 16 17 PERS -4.3.3V. 16 17 PERS -4.3.3V. 16 18 PERS NA 42 20 PERS NA 42 21 PERS NA 43 32 GAD NA 43 33 PETS DNA	1	GND	+3.3V	2
5 PERD3 NA 6 5 PERD3 NA 6 10 PERD3 NA 6 11 PERD3 NA 10 12 PERD3 NA 10 13 PETD3 NA 10 14 PERD2 NA 10 15 PERD2 NA 20 16 PERD2 NA 20 17 PERD2 NA 20 21 GND NA 22 22 PERD2 NA 20 23 PERD1 NA 22 23 PERD1 NA 32 35 PERD1 NA 32 35 PERD1 NA 32 36 PERD NA 42 41 PER0 NA 42 42 PERD2 NA 44 9 PERD2 NA 44	3	GND	+3.3V	4
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9 0 SAND SATA, LED 10 9 1 PETD3 4-3.3V 12 13 PETD3 4-3.3V 12 13 PETD3 4-3.3V 15 15 PETD3 4-3.3V 16 15 PETD3 4-3.3V 16 15 PETD2 NA 20 19 PETD2 NA 20 20 PETD2 NA 20 21 PETD2 NA 20 22 PETD2 NA 20 231 PETD1 NA 32 233 PETD1 NA 32 234 PETD1 NA 32 235 PETD1 NA 44 4 PETD0 NA 42 435 GND NA 44 45 GND NA 44 46 PETC4 NA 45 54 GND	7	PERp3	NA	8
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13 Ferp3 43.3V 14 13 Ferp3 43.3V 16 14 A 43.4V 16 17 Ferb2 43.3V 16 17 Ferb2 43.3V 16 17 Ferb2 NA 42 18 Ferb2 NA 42 21 Ferb2 NA 42 22 Ferb2 NA 42 23 Ferb1 NA 32 31 Ferb1 NA 32 33 Ferb1 NA 32 35 Ferb1 Devalue 44 40 Ferb2 NA 44 41 Ferb2 NA 44 42 Ferb2 NA 44 44 Ferb2 NA 44 45 Forb NA 46 6 Ferb2 NA 46 6 Forb NA 46	11	PETn3	+3.3V	12
15 CMD +3.3.9.1 16 15 CMD +3.3.9.1 16 16 CMD +3.3.9.1 16 17 CMD +3.2.9.1 16 18 CMD NA 20 19 CMD NA 20 21 CMD NA 20 22 CMD NA 20 23 PER1 NA 30 33 PER1 NA 30 34 PER0 NA 42 35 PER0 NA 42 43 FCMD NA 42 44 PER0 NA 42 45 CMD NA 44 46 CMD NA 44 47 PER0 NA 44 46 CMD NA 45 50 PECL4p NA 45 51 FMO NA 46	13	PETp3	+3.3V	14
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379 PErp1 DEVSLP 38. 379 PKD1 DEVSLP 38. 40. 381 PKn0 NA 40. 415 PKn0 NA 40. 416 PKD0 NA 46. 420 PETn0 NA 46. 47 PETn0 NA 46. 48 PETD PRERTI 50. 510 PECAT 50. 57. 600 OKD NA 46. 67 PECAT 80. 67. 67 OKD NA 40. 67 PAC +3.3.9. 70. 67 PAC +3.3.9. 70. 715 GND 3.39. 74.	35	PETn1	NA	36
SND OND NA 40 981 PERAD NA 42 453 FORD NA 42 454 FORD NA 44 457 FORD NA 44 458 FORD NA 44 450 FORD NA 44 450 FORD NA 44 451 FORD NA 44 451 FORD FORD NA 501 FORD CLUREON 52 51 FEFCLM NA 56 57 FORD NA 56 57 FA NA 56 601 FORD 3.3.3.9.7 72 71 GND 3.3.9.7 72 73 GND 3.3.9.7 72 75 GND 3.3.9.7 72	37	PETp1	DEVSLP	38
41 PERR0 NA 42 43 PERR0 NA 44 44 PERR0 NA 44 45 PERD0 NA 44 46 PERD0 NA 48 46 PERD0 NA 48 46 PERD0 NA 48 45 PERD0 PLRME 43 45 PERD0 PLRME 48 50 PERCLAN WAKE9 54 57 PERON NA 56 57 NA NA 56 67 NA 57 56 77 S GND 3.3V	39	GND	NA	40
43 DERp0 NA 44 43 FORD NA 44 44 FERD NA 44 47 PETRO NA 44 47 PETRO PERDIT 50 58 PEFCLOR WAKEF 45 55 PEFCLOR NA 56 57 FM NA 56 57 FM NA 56 57 FM NA 56 57 FM NA 56 57 GND 3.3/ 72 71 GND 3.3/ 72 73 GND 3.3/ 74	41	PERn0	NA	42
AP AP AP 427 PETnO NA 48 451 DETnO NA 48 451 DETDO PERETIR 50 51 OFDO CLURECIPI 52 53 DEPECLAN WAKEI 54 57 OFNO NA 56 67 NA NA 56 67 NA NA 56 671 NA NA 56 671 OND 43.3.3.9.7 72 73 GND 43.3.9.7 72 73 GND 43.3.9.7 74	43	PERp0	NA	44
47 PETr0 NA 48, 47 PETr3 50, PERST# 50, 51 PETCLWP PERST# 50, 53 SPEFCLKN WAKEF# 54, 55 PEFCLKP NA 56, 57 GND NA 56, 67 NA NA 68, 67 NA NA 86, 67 GND 4.3.3V 70, 71 GND 4.3.3V 74, 75 GND 4.3.3V 74,	45	GND	NA	46
49 PPETp0 PPERST# 50. 51 GND CLKHECH 52. 53 PEFCLKn WAKE# 54. 55 PEFCLKn NA 56. 57 GND NA 56. 67 NA NA 68. 67 PEDET +3.3V 77. 73 GND +3.3V 74.	47	PETn0	NA	48
51 GND CLKREO/# 52 S3 PEFCLKP VAKKEI 54 S5 PEFCLKP NA 56 F7 GND NA 58 67 NA NA 58 77 GND NA 58 67 NA NA 68 99 PEDET +3.3V 72 73 GND +3.3V 74 75 GND +43.3V 74	49	PETp0	PERST#	50
S3 PEFCLKn WAKE# 54 S3 FPECLKn NA 58 S7 GND NA 58 67 NA NA 68 67 PEDET *3.3V 70 68 FOID *3.3V 72 73 GND *3.3V 74	51	GND	CLKREQ#	52
55 PEFCLKp NA 56 57 GND NA 58 67 NA NA 68 69 PEDET *3.3V 70 71 GND *3.3V 72 73 GND *3.3V 74 75 GND *3.3V 74	53	PEFCLKn	WAKE#	54
57 GND NA 58 67 NA NA 58 69 PEDET +3.3V 70 71 GND +3.3V 72 73 GND +3.3V 74 75 GND +3.3V 74	55	PEFCLKp	NA	56
King King <th< td=""><td>57</td><td>GND</td><td>NA</td><td>58</td></th<>	57	GND	NA	58
67 NA NA 68 69 PEDET +3.3V 70 71 GND +3.3V 72 73 GND +3.3V 72 75 GND +3.3V 74	T			T
69 PEDET +3.3V 70 71 GND +3.3V 72 73 GND +3.3V 74 75 GND	67	NA	NA	68
71 GND +3.3V 72 73 GND +3.3V 74 75 GND	69	PEDET	+3.3V	70
73 GND +3.3V 74 75 GND	71	GND	+3.3V	72
75 GND	73	GND	+3.3V	74
	75	GND		

M.2 Key-B Socket (M2_B1)

Pin	Signal Name	Signal Name	Pin
1	NA	+3.3V	2
3	GND	+3.3V	4
5	GND	FuLL_Card_ Power_off	6
7	USB_D+	W_DISABLE	8
9	USB D-	WWAN LED#	10
11	GND		
21	GND	NA	20
23	NA	NA	22
25	NA	NA	24
27	GND	NA	26
29	USB3 RX-	NA	28
31	USB3 RX+	UIM RESET	30
33	GND	UIM CLK	32
35	USB3 TX-	UIM DATA	34
37	USB3 TX+	UIM PWR	36
39	GND	NA	38
41	PERn0	NA	40
43	PERp0	NA	42
45	GND	NA	44
47	PETn0	NA	46
49	PETP0	NA	48
51	GND	PERST#	50
53	PEFCLKn	CLKREQ#	52
55	PEFCLKp	WAKE#	54
57	GND	NA	56
59	NA	NA	58
61	NA	NA	60
63	NA	NA	62
65	NA	NA	64
67	NA	NA	66
69	NA	NA	68
71	GND	+3.3V	70
73	GND	+3.3V	72
75	NA	+3.3V	74

SIM socket:

1 x SIM socket connected to M.2 key B.

2.5 Jumpers Setup

The illustration shows how jumpers are setup. When the jumper cap is placed on pins, the jumper is "Short". If no jumper cap is placed on pins, the jumper is "Open". The illustration shows a 3-pin jumper whose pin1 and pin2 are "Short" when jumper cap is placed on these 2 pins.



Jumper	S	etting	Description
Clear CMOS Jumpers			CLRMOS1:
(3-pin CLRMOS1)	1_2	2_3	1-2 : Normal
(see p. 9, No. 19)	Default	Clear CMOS	2-3 : Clear CMOS

Note: CLRMOS1 allows you to clear the data in CMOS. To clear and reset the system parameters to default setup, please turn off the computer and unplug the power cord from the power supply. After waiting for 15 seconds, use a jumper cap to short pin2 and pin3 on CLRMOS1 for 5 seconds. However, please do not clear the CMOS right after you update the BIOS. If you need to clear the CMOS when you just finish updating the BIOS, you must boot up the system first, and then shut it down before you do the clear-CMOS action. Please be noted that the date, time and user default profile will be cleared only if the CMOS battery is removed.

COM Port PWR Setting Jumpers		1-2 : +5V	
(3-pin PWR_COM1 (For COM Port1))	$\Box \circ \circ$	2-3 : +12V	
(3-pin PWR_COM3 (For COM Port3))	1 2 3		
(see p. 9, No. 13)			

DACC1

(2-pin DACC1) (see p. 9, No. 7)

Note: Auto clear CMOS when system boot improperly.

Digital Input / Output Default Val	lue Setting
(3-pin JGPIO_SET1)	n o o
(see p. 9, No. 8)	1 2 3

1-2 : Pull-High 2-3 : Pull-Low

Open : no ACC Short : ACC

Digital Input / Output Power Sele	ect	1-2 : +12V
(3-pin JGPIO_PWR1)		2-3 : +5V
(see p. 9, No. 9)	1 2 3	

2.6 Onboard Headers and Connectors



Onboard headers and connectors are NOT jumpers. Do NOT place jumper caps over these headers and connectors. Placing jumper caps over the headers and connectors will cause permanent damage of the motherboard!

CPU Fan Connector

(4-pin CPU_FAN1) (see p. 9 No. 25)



Please connect the CPU fan cable to the connector and match the black wire to the ground pin.



Though this motherboard provides 4-Pin CPU fan (Quiet Fan) support, the 3-Pin CPU fan still can work successfully even without the fan speed control function. If you plan to connect the 3-Pin CPU fan to the CPU fan connector on this motherboard, please connect it to Pin 1-3.

Chassis Fan Connector

(4-pin CHA_FAN1) (see p. 9 No. 26)



Please connect the chassis fan cable to the connector and match the black wire to the ground pin.



Though this motherboard provides 4-Pin chassis fan (Quiet Fan) support, the 3-Pin chassis fan still can work successfully even with the fan speed control function. If you plan to connect the 3-Pin chassis fan to the chassis fan connector on this motherboard, please connect it to Pin 1-3.

System Panel Header (9-pin PANEL1) (see p. 9, No. 23)



This header accommodates several system front panel functions.



Connect the power switch, reset switch and system status indicator on the chassis to this header according to the pin assignments below. Note the positive and negative pins before connecting the cables.

PWRBTN (Power Switch):

Connect to the power switch on the chassis front panel. You may configure the way to turn off your system using the power switch.

RESET (Reset Switch):

Connect to the reset switch on the chassis front panel. Press the reset switch to restart the computer if the computer freezes and fails to perform a normal restart.

PLED (System Power LED):

Connect to the power status indicator on the chassis front panel. The LED is on when the system is operating. The LED keeps blinking when the system is in S1/S3 sleep state. The LED is off when the system is in S4 sleep state or powered off (S5).

HDLED (Hard Drive Activity LED):

Connect to the hard drive activity LED on the chassis front panel. The LED is on when the hard drive is reading or writing data.

The front panel design may differ by chassis. A front panel module mainly consists of power switch, reset switch, power LED, hard drive activity LED, speaker and etc. When connecting your chassis front panel module to this header, make sure the wire assignments and the pin assign-ments are matched correctly.

COM Port Headers

(9-pin COM1, 2, 3, 4: see p. 9, No. 11)

1	
0 0000	
2	

PIN	Signal Name								
1	DDCD#	3	TTXD	5	GND	7	RRTS#	9	PWR
2	RRXD	4	DDTR#	6	DDSR#	8	CCTS#		

* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to below table for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to page 31 for details.

COM1, 2 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	TX+	RTX+
3	TXD	RX+	N/A
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	PWR	PWR	PWR



LVDS Panel Connector*

(40-pin LVDS1) (see p. 9 No. 15)



PIN	Signal Name	PIN	Signal Name
39	LCD_BLT_VCC	40	LCD_BLT_VCC
37	CON_LBKLT_CTL	38	LCD_BLT_VCC
35	GND	36	CON_LBKLT_EN
33	LVDS_B_CLK#	34	LVDS_B_CLK
31	LVDS_B_DATA3	32	GND
29	DPLVDD_EN	30	LVDS_B_DATA3#
27	LVDS_B_DATA2#	28	LVDS_B_DATA2
25	LVDS_B_DATA1	26	GND
23	GND	24	LVDS_B_DATA1#
21	LVDS_B_DATA0#	22	LVDS_B_DATA0
19	LVDS_A_CLK	20	GND
17	GND	18	LVDS_A_CLK#
15	LVDS_A_DATA3#	16	LVDS_A_DATA3
13	LVDS_A_DATA2	14	GND
11	GND	12	LVDS_A_DATA2#
9	LVDS_A_DATA1#	10	LVDS_A_DATA1
7	LVDS_A_DATA0	8	PD (Panel Detection)
5	LDDC_DATA	6	LVDS_A_DATA0#
3	+3.3V	4	LDDC_CLK
1	LCD_VCC	2	LCD_VCC

* PD (Panel Detection): Connect this pin to LVDS Panel's Ground pin to detect Panel detection.

*eDP Connector (on the Backside of PCB) Signal Name

NA

PIN 40



39	LCD_BLT_VCC
38	LCD_BLT_VCC
37	LCD_BLT_VCC
36	LCD_BLT_VCC
35	SMB_CLK_MAIN
34	SMB_DATA_MAIN
33	eDP_BKLTCTL_R
32	eDP_BKLTEN
31	GND
30	GND
29	GND
28	GND
27	eDP_HPD_CON
26	GND
25	GND
24	GND
23	GND
22	NA
21	LCD_VCC
20	LCD_VCC
19	LCD_VCC
18	LCD_VCC
17	GND
16	eDP_AUX#_CON
15	eDP_AUX_CON
14	GND
13	eDP_TX0_CON
12	eDP_TX#0_CON
11	GND
10	eDP_TX1_CON
9	eDP_TX#1_CON
8	GND
7	eDP_TX2_CON
6	eDP_TX#2_CON
5	GND
4	eDP_TX3_CON
3	eDP_TX#3_CON
2	GND
1	NA
_	

SPDIF Header

(3-pin SPDIF1) (see p. 9, No. 32)



SPDIF header, providing SPDIF audio output to HDMI VGA card, allows the system to connect HDMI Digital TV/ projector/LCD devices. Please connect the SPDIF connector of HDMI VGA card to this header.

SATA Power Connector (SATA_PWR1) (see p. 9 No. 5)		Please co power cab	nnect a le to th	is co	TA onnector.
ATX Power Connector (4-pin DC_4PIN1) (see p. 9 No. 30)	4 3 2 1	Please con power sup connector. 1-2 : GND 3-4 : DC Ir	nnect a ply to t nput	DC his	
UPS Module Power Inpu (2-pin TO_UPS1) (see p. 9 No. 31)	ut Connector	Pin1 : GNI Pin2 : DC	D Input		
Digital Input/Output Pin I (10-pin JGPIO1) (see p. 9 No. 10)	Header 2 00000 00000				
	PIN Signal Name PIN Signal Name PIN 2 GPP_H23 4 GPP_I10 6 1 SIO_GP71 3 SIO_GP72 5	Signal NamePINGPP_E58SIO_ GP737	Signal Name GPP_E6 SIO_ GP74	PIN 10 9	Signal Name GND JGPIOPWR_ R
Inverter Power Control V (6-pin BLT_PWR1) (see p. 9 No. 14)	Nafer		PIN 6 5 4 3 2 1	Sit LCE LCE CON	gnal Name D_BLT_VCC D_BLT_VCC 4_LBKLT_EN _LBKLT_CTL GND GND
Backlight Volume Contro (7-pin BLT_VOL1) (see p. 9 No. 16)			PIN 7 6 5 4 3 2 1	GP	ignal Name GND GND BLT_DW BLT_UP PWRDN IO_VOL_DW PIO_VOL_UP
Backlight Power Select ((5-pin BKT_PWR1) (see p. 9 No. 12)	(LCD_BLT_VCC) 1 00000	1-2 : LCD_ 2-3 : LCD_ 4-5 : LCD_	_BLT_V _BLT_V _BLT_V	/CC: /CC: /CC:	: +5V : +12V : DC_IN

SMBUS_TEST1 (4-pin SMBUS_TEST1) (see p. 9 No. 24)	GND SMB_DATA_MAIN SMB_CLK_MAIN	1
Power Adapter (4-pin POWER_ADAPTER) (see p. 9 No. 27)		PINSignal Name1GND25VA_CONTROL35VA4GND
AT_TEST1_PCIE_PWR1 (4-pin AT_TEST1_PCIE_PWR1) (see p. 9 No. 22)	1 0 2 0 3 4	Signal Name AT_TEST1 PCIE_PWR1 (For VGA Power Card Only): Pin3: PSON# Pin4: GND
Panel Power Select (LCD (5-pin PNL_PWR1) (see p. 9 No. 17)	_VCC) 1 00000	1-2 : LCD_VCC: +3V 2-3 : LCD_VCC: +5V 4-5 : LCD_VCC: +12V
Inverter Power Control Wa (6-pin BLT_PWR2) (see p. 9 No. 18)	Afer 1 00000 PIN Signal PIN Signal PIN Signal Name PIN Name 1 GND 2 GND 3 EBKIT CTL	PIN Signal Name PIN Signal Name PIN Signal Name 4 EDP LBKLT_ EN 5 +12_ FUSE 6 +12_ FUSE
PWR_BAT1_SIO_AT1 (4-pin PWR_BAT1_SIO_AT1) (see p. 9 No. 21)		PIN Signal Name 1 PWR_BAT1: Open: Normal 2 Short: Charge Battery 3 SIO_AT1: Open: ATX Mode 4 Short: AT Mode
Chassis Intrusion Header (4-pin Cl1_2: see p. 9 No. 20)	PIN Signal Name 0 1 Cl1 Signal 2 GND 3 3 Cl2 Signal 4	This motherboard supports CASE OPEN detection feature that detects if the chassis cover has been removed. This feature requires a chassis with chassis intrusion detection design.

2.7 Installation of ROM Socket



* Do not apply force to the actuator cover after ic inserted.

* Do not apply force to actuator cover when it is opening over 120 degree, Otherwise, the actuator cover may be broken.



* The yellow dot (Pin1) on the ROM must be installed at pin1 position of the socket (white arrow area).

* Make sure the white dot on the ROM is installed outwards of the socket.

* For further details of how to install ROM, please refer to ASRI website.

Warning: If the installation does not follow as the picture, then it may cause severe damage to chipset & MB.

Chapter 3: UEFI SETUP UTILITY

3.1 Introduction

This section explains how to use the UEFI SETUP UTILITY to configure your system. The UEFI chip on the motherboard stores the UEFI SETUP UTILITY. You may run the UEFI SETUP UTILITY when you start up the computer. Please press <F2> or during the Power-On-Self-Test (POST) to enter the UEFI SETUP UTILITY, otherwise, POST will continue with its test routines.

If you wish to enter the UEFI SETUP UTILITY after POST, restart the system by pressing <Ctl> + <Alt> + <Delete>, or by pressing the reset button on the system chassis. You may also restart by turning the system off and then back on.



Because the UEFI software is constantly being updated, the following UEFI setup screens and descriptions are for reference purpose only, and they may not exactly match what you see on your screen.

3.1.1 UEFI Menu Bar

The top of the screen has a menu bar with the following selections:

	-
Main	To set up the system time/date information
Advanced To set up the advanced UEFI features	
H/W Monitor To display current hardware status	
Security	To set up the security features
Boot	To set up the default system device to locate and load the
	Operating System
Exit	To exit the current screen or the UEFI SETUP UTILITY

Use < \leftarrow > key or < \rightarrow > key to choose among the selections on the menu bar, and then press <Enter> to get into the sub screen. You can also use the mouse to click your required item.

3.1.2 Navigation Keys

Please check the following table for the function description of each navigation key.

Navigation Key(s)	Function Description
← / →	Moves cursor left or right to select Screens
↑/↓	Moves cursor up or down to select items
+ / -	To change option for the selected items
<enter></enter>	To bring up the selected screen
<f1></f1>	To display the General Help Screen
<f7></f7>	Discard changes
<f9></f9>	To load optimal default values for all the settings
<f10></f10>	To save changes and exit the UEFI SETUP UTILITY
<f12></f12>	Print screen
<esc></esc>	To jump to the Exit Screen or exit the current screen

3.2 Main Screen

When you enter the UEFI SETUP UTILITY, the Main screen will appear and display the system overview.



3.3 Advanced Screen

In this section, you may set the configurations for the following items: CPU Configuration, Chipset Configuration, Storage Configuration, Super IO Configuration, ACPI Configuration, USB Configuration and Trusted Computing.



Setting wrong values in this section may cause the system to malfunction.

Instant Flash

Instant Flash is a UEFI flash utility embedded in Flash ROM. This convenient UEFI update tool allows you to update system UEFI without entering operating systems first like MS-DOS or Windows[®]. Just launch this tool and save the new UEFI file to your USB flash drive, floppy disk or hard drive, then you can update your UEFI only in a few clicks without preparing an additional floppy diskette or other complicated flash utility. Please be noted that the USB flash drive or hard drive must use FAT32/16/12 file system. If you execute Instant Flash utility, the utility will show the UEFI files and their respective information. Select the proper UEFI file to update your UEFI, and reboot your system after UEFI update process completes.

3.3.1 CPU Configuration



Intel Hyper Threading Technology

Intel Hyper Threading Technology allows multiple threads to run on each core, so that the overall performance on threaded software is improved.

Active Processor P-Cores

Select the number of cores to enable in each processor package.

Active Processor E-Cores

Select the number of E-Cores to enable in each processor package.

CPU C States Support

Enable CPU C States Support for power saving. It is recommended to keep C3, C6 and C7 all enabled for better power saving.

Intel Virtualization Technology

When this option is set to [Enabled], a VMM (Virtual Machine Architecture) can utilize the additional hardware capabilities provided by Vanderpool Technology. This option will be hidden if the installed CPU does not support Intel Virtualization Technology.

Intel SpeedStep Technology

Intel SpeedStep technology is Intel's new power saving technology. Processors can switch between multiple frequencies and voltage points to enable power saving. The default value is [Enabled]. Configuration options: [Enabled] and [Disabled]. If you install Windows[®] OS and want to enable this function, please set this item to [Enabled]. This item will be hidden if the current CPU does not support Intel SpeedStep technology.



Please note that enabling this function may reduce CPU voltage and lead to system stability or compatibility issues with some power supplies. Please set this item to [Disabled] if above issues occur.

Intel Turbo Boost Technology

Use this item to enable or disable Intel Turbo Boost Mode Technology. Turbo Boost Mode allows processor cores to run faster than marked frequency in specific conditions. The default value is [Enabled].

CPU Thermal Throttling

You may select [Enabled] to enable CPU internal thermal control mechanism to keep the CPU from overheating.

3.3.2 Chipset Configuration

Advanced	Aptio Setup – AMI	
ME Firmware Version VT-d Capability	16.0.15.1620 Supported	Select a primary VGA.
Primary Graphics Adapter Above 46 Decoding VT-d	[PCI Express] [Disabled] [Enabled]	
PCIE1 Bandwidth Mode PCIE1 Link Speed	[x16 Mode] [Auto]	
Share Memory IGPU Multi-Monitor Bender Standhu	(Auto) [Disabled] [Disabled]	
Active LVDS	[Disabled]	↔: Select Screen 14: Select Item
Onboard LAN1 Onboard LAN2	[Enabled] [Enabled]	Enter: Select +/-: Change Option E1: General Helm
Onboard HD Audio	[Enabled]	F7: Discard Changes F9: Load UEFI Defaults
Deep Sleep Restore on AC/Power Loss	(Disabled) (Power Off)	F10: Save and Exit ESC: Exit
Ver	sion 2.22.1284 Copyright (C) 2022 AMI

Primary Graphics Adapter

This allows you to select [Onboard] or [PCI Express] as the boot graphic adapter priority. The default value is [PCI Express].

Above 4G Decoding

Enable or disable 64bit capable Devices to be decoded in Above 4G Address Space (only if the system supports 64 bit PCI decoding).

VT-d

Use this to enable or disable Intel[®] VT-d technology (Intel[®] Virtualization Technology for Directed I/O). The default value of this feature is [Disabled].

PCIE1 Bandwidth Mode

Select the bandwidth mode for PCIE1.

PCIE1 Link Speed

Select the link speed for PCIE1.

Share Memory

Configure the size of memory that is allocated to the integrated graphics processor when the system boots up.

IGPU Multi-Moniter

Select disable to disable the integrated graphics when an external graphics card is installed. Select enable to keep the integrated graphics enabled at all times.

Render Standby

Power down the render unit when the GPU is idle for lower power consumption.

Active LVDS

Use this to enable or disable the LVDS. The default value is [Disabled]. Set the item to [enable]. Then press <F10> to save the setting and restart the system. Now the default value of Active LVDS is changed to ENABLE

(F9 load default is also set to ENABLE). Change the setting from [Enable] to [Disable], and then press <F10> to save the setting and restart the system. Likewise, the default value of Active LVDS is changed to DISABLE (F9 load default is also set to DISABLE)

Onboard LAN1

This allows you to enable or disable the Onboard LAN1 feature.

Onboard LAN2

This allows you to enable or disable the Onboard LAN2 feature.

Onboard HD Audio

Select [Auto], [Enabled] or [Disabled] for the onboard HD Audio feature. If you select [Auto], the onboard HD Audio will be disabled when PCI Sound Card is plugged.

Deep Sleep

Mobile platforms support Deep S4/S5 in DC only and desktop platforms support Deep S4/S5 in AC only. The default value is [Disabled].

Restore on AC/Power Loss

Select the power state after a power failure. If [Power Off] is selected, the power will remain off when the power recovers. If [Power On] is selected, the system will start to boot up when the power recovers.

3.3.3 Storage Configuration



VMD Configuration

This item allows you to enable or disable the Intel VMD support function.

SATA Controller(s)

Use this item to enable or disable the SATA Controller feature.

SATA Mode Selection

Use this to select SATA mode. The default value is [AHCI Mode].



AHCI (Advanced Host Controller Interface) supports NCQ and other new features that will improve SATA disk performance but IDE mode does not have these advantages.

Hybrid Storage Detection and Configuration Mode

Use this item to enable or disable Hybrid Storage Detection and Configuration Mode.

SATA Aggressive Link Power Management

Use this item to configure SATA Aggressive Link Power Management.

Hard Disk S.M.A.R.T.

Use this item to enable or disable the S.M.A.R.T. (Self-Monitoring, Analysis, and Reporting Technology) feature. Configuration options: [Disabled] and [Enabled].

3.3.4 Super IO Configuration

Advanced	Aptio Setup – AMI	
ODH1 Type Select ODH2 Type Select COH4 COH4 WDT Timeout Reset	(Enabled) (R0232) (Enabled) (Enabled) (Enabled) (Disabled)	Enable on Disable COM1 10-SFBh: IRQ-4:
		+t: Select Scheen 14: Select Trem Enter: Select +/-: Change Option P: General Help P7: Discard Changes P9: Load UFT Defaults P10: Save and Exit ESD: Exit
	Vaccion 2 22 1294 Convolution	(P) 2022 ANT

COM1 Configuration

Use this to set parameters of COM1.

Type Select

Use this to select COM1 port type: [RS232], [RS422] or [RS485].

COM2 Configuration

Use this to set parameters of COM2.

Type Select

Use this to select COM2 port type: [RS232], [RS422] or [RS485].

COM3 Configuration

Use this to set parameters of COM3.

COM4 Configuration

Use this to set parameters of COM4.

WDT Timeout Reset

Use this to set the Watch Dog Timer.

3.3.5 ACPI Configuration

Advanced	Aptio Setup – AMJ	
Norreco Suspend to RM POIE Devices Power On RTC Alarm Power On	(futo) (Districted) (By OS)	It is recommended to select auto for ACPE S3 power saving.
		F1: General Mein F7: Discard Changes F9: Load UEFI Defaults F10: Save and Exit E50: Exit

Suspend to RAM

Use this item to select whether to auto-detect or disable the Suspend-to-RAM feature. Select [Auto] will enable this feature if the OS supports it.

PCIE Devices Power On

Use this item to enable or disable PCIE devices to turn on the system from the power-soft-off mode.

RTC Alarm Power On

Use this item to enable or disable RTC (Real Time Clock) to power on the system.

3.3.6 USB Configuration

Advanced	Aptio Setup – AMI	
USB Four Control M.2 KeyB USB function	(Default Setting) (Enabled)	Alweys enabled: Enable USB power in SU/SU/AFX5, Default setting: Foode USB power in SU/SS. disable USB power in SU/SS. **: Select Screen 11: Select Item Enter: Select **: Charge Ontion F1: General Help F2: Disard Charges F30: Save and Exit ESD: Exit
W	ersion 2.22.1284 Copyright (C)	2022 AMI

USB Power Control

Use this option to control USB power.

M.2 Key_B USB Configuration

Enable or disable M.2 Key_B USB Configuration.

3.3.7 Trusted Computing



Security Device Support

Enable or disable BIOS support for security device.

3.4 Hardware Health Event Monitoring Screen

In this section, it allows you to monitor the status of the hardware on your system, including the parameters of the CPU temperature, motherboard temperature, CPU fan speed, chassis fan speed, and the critical voltage.

Aptio Setup – AMI Main Advanced <mark>H/W Monitor</mark> Security Boot Exit				
Hardware Health Event Monito	oring : +45 ℃	Quiet Fan Function Control		
M/B Temperature CPU_FAN1 Speed	: +32 °C : 3239 RPM			
CHA_FAN1 Speed	: N/A : +3.392 V			
+3VSB VCORE VCCM	: +3.328 V : +1.160 V : +1.200 V			
VBAT DC_IN	: +2.976 V : +11.704 V	↔: Select Screen †4: Select Item		
CPU_FAN1 Setting CHA_FAN1 Setting Case Open Feature	[Full On] [Full On] [Disabled]	Enter: Select +/-: Change Option F1: General Help F7: Discard Changes		
		F9: Load UEFI Defaults F10: Save and Exit ESC: Exit		
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CPU_FAN1 Setting

This allows you to set CPU fan 1's speed. Configuration options: [Full On] and [Automatic Mode]. The default value is [Full On].

CHA_FAN1 Setting

This allows you to set chassis fan 1's speed. Configuration options: [Full On] and [Automatic Mode]. The default value is [Full On].

Case Open Feature

This allows you to enable or disable case open detection feature. The default is value [Disabled].

Clear Status

This option appears only when the case open has been detected. Use this option to keep or clear the record of previous chassis intrusion status.

3.5 Security Screen

In this section, you may set, change or clear the supervisor/user password for the system.



Supervisor Password

Set or change the password for the administrator account. Only the administrator has authority to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

User Password

Set or change the password for the user account. Users are unable to change the settings in the UEFI Setup Utility. Leave it blank and press enter to remove the password.

Secure Boot

Use this item to enable or disable support for Secure Boot.

3.6 Boot Screen

In this section, it will display the available devices on your system for you to configure the boot settings and the boot priority.

Main Advanced H/W Monitor	Aptio Setup - AMI Security Boot Exit		
Boot Option Priorities Boot Option #1		Sets the system boot order	
Boot From Onboard LAN	[Disabled]		
Setup Prompt Timeout Bootup Num-Lock Full Screen Logo	1 [On] [Enabled]		
		++: Select Screen H: Select Item Enter: Select V-: Change Option F: General Hein F: General Hein F: Load Ure Defaults F: Load Ure Defaults F: Load Ure Defaults F: Select Exit	
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Boot From Onboard LAN

Use this item to enable or disable the Boot From Onboard LAN feature.

Setup Prompt Timeout

This shows the number of seconds to wait for setup activation key. 65535(0XFFFF) means indefinite waiting.

Bootup Num-Lock

If this item is set to [On], it will automatically activate the Numeric Lock function after boot-up.

Full Screen Logo

Use this item to enable or disable OEM Logo. The default value is [Disabled].

3.7 Exit Screen



Save Changes and Exit

When you select this option, it will pop-out the following message, "Save configuration changes and exit setup?" Select [OK] to save the changes and exit the UEFI SETUP UTILITY.

Discard Changes and Exit

When you select this option, it will pop-out the following message, "Discard changes and exit setup?" Select [OK] to exit the UEFI SETUP UTILITY without saving any changes.

Discard Changes

When you select this option, it will pop-out the following message, "Discard changes?" Select [OK] to discard all changes.

Load UEFI Defaults

Load UEFI default values for all the setup questions. F9 key can be used for this operation.

Launch EFI Shell from filesystem device

Attempts to Launch EFI Shell application (Shell64.efi) from one of the available filesystem devices.

Chapter 4: Software Support

4.1 Install Operating System

This motherboard supports various Microsoft[®] Windows[®] operating systems: 11 64bit / 10 64-bit. Because motherboard settings and hardware options vary, use the setup procedures in this chapter for general reference only. Refer your OS documentation for more information.