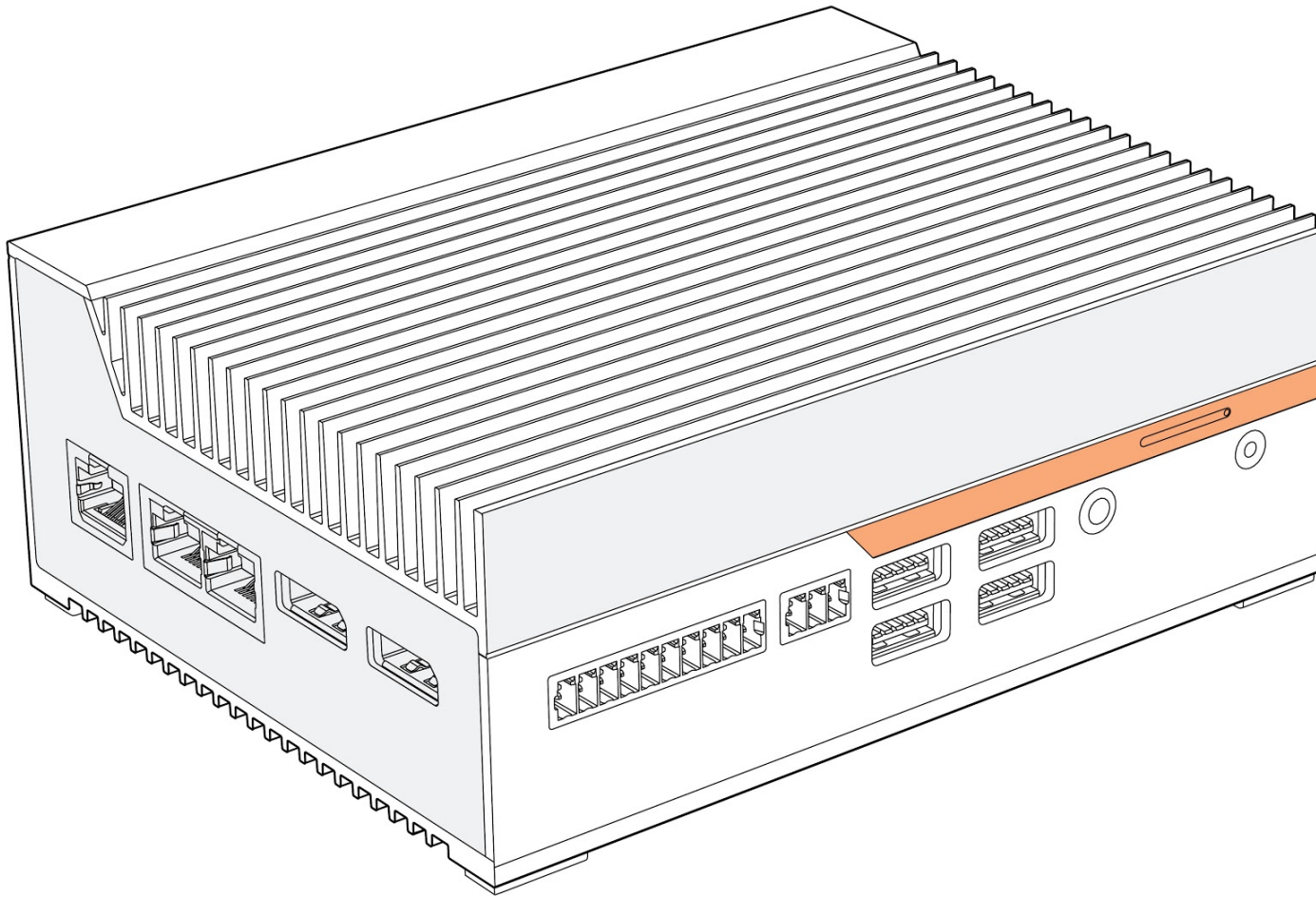


Karbon 300 BIOS Manual



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Main Page

BIOS Vendor

Type	Information
Found on BIOS Page	Main Page
Description	Displays BIOS vendor

Core Version

Type	Information
Found on BIOS Page	Main Page
Description	Displays current core version

Compliance

Type	Information
Found on BIOS Page	Main Page
Description	Displays the BIOS compliance

BIOS Version

Type	Information
Found on BIOS Page	Main Page
Description	Displays the current BIOS version

Build Date

Type	Information
Found on BIOS Page	Main Page
Description	Displays the current build date in MM/DD/YYYY

BXT SOC

Type	Information
Found on BIOS Page	Main Page

Description	Displays the SOC version
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MRC Version

Type	Information
Found on BIOS Page	Main Page
Description	Displays the MRC version

PUNIT FW

Type	Information
Found on BIOS Page	Main Page
Description	Displays the PUNIT FW version

PMC FW

Type	Information
Found on BIOS Page	Main Page
Description	Displays the PMC FW version

TXE FW

Type	Information
Found on BIOS Page	Main Page
Description	Displays the TXE FW version

ISH FW

Type	Information
Found on BIOS Page	Main Page
Description	Displays the ISH FW version

GOP

Type	Information
Found on BIOS Page	Main Page
Description	Displays the GOP version

CPU Flavor

Type	Information
Found on BIOS Page	Main Page
Description	Displays the CPU flavor

Board ID

Type	Information
Found on BIOS Page	Main Page
Description	Displays the Board ID

Fab ID

Type	Information
Found on BIOS Page	Main Page
Description	Displays the Fab ID

Total Memory

Type	Information
Found on BIOS Page	Main Page
Description	Displays the installed memory size

Memory Speed

Type	Information
Found on BIOS Page	Main Page
Description	Displays clock speed of memory

System Language

Type	Information
Found on BIOS Page	Main Page
Description	Select the current default language

System Date

Type	Information
Found on BIOS Page	Main Page
Description	Display the date in MM:DD:YYYY. Valid range is from 1 to 12, 1 to 31, 2005 to 2099. Use Tab to switch between date elements

System Time

Type	Information
Found on BIOS Page	Main Page
Description	Display the time in HH:MM:SS. Valid range is from 0 to 23, 0 to 59, 0 to 59. Use Tab to switch between time elements

Advanced Page

Watchdog Timer

Type	Configurable Setting
Found on BIOS Page	Advanced Page
Description	Disabled: disable TCO watchdog timer, halt timer count: no reset will occur. Enabled: enable TCO watchdog timer, start timer count

BIOS Timer

Type	Configurable Setting
Found on BIOS Page	Advanced Page
Description	Set BIOS watchdog timer. Default value of 60. Possible values from 30 - 255

Advanced Page > Driver Health

Press Enter to view the sub-sections for Driver Health

Intel(R) PRO /1000 7.0.06 PCI-E

Type	Information
Found on BIOS Page	Advanced Page > Driver Health
Description	Provides the health status for the drivers/controllers. Press Enter when selected to go into the associated Sub-Menu

Advanced Page > NCT5524D Super IO Configuration

Super IO Chip

Type	Information
Found on BIOS Page	Advanced Page > NCT5524D Super IO Configuration
Description	Displays the Super IO Chip type

Serial Port 1 & Serial Port 2

Type	Configurable Setting
Found on BIOS Page	Advanced Page > NCT5524D Super IO Configuration
Description	Enables or Disables serial port (COM). Use +/- to change option

Serial Port Mode

Type	Configurable Setting
Found on BIOS Page	Advanced Page > NCT5524D Super IO Configuration
Description	Displays the current serial port mode. Possible values are: 1T/1R RS-422, 3T/5R RS-232, 1T/1R RS-485 TX ENABLE Low Active, 1T/1R RS-485 with termination resistor TX ENABLE Low Active, 1T/1R RS-422 with termination resistor, Disabled

Advanced Page > Hardware Monitor

VR Temperature

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the temperature of the VR. Ranges from -40 to 105°C

Memory Temperature

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the temperature of the memory. Ranges from -40 to 105°C

VCORE

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the voltage of the VCORE. Ranges from .5 to 1.5V

3VSB

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the voltage of the 3VSB. Ranges from 3.135 to 3.465V

VSM

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the voltage of the VSM. Ranges from 1.14 to 1.26V

VCC3

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the voltage of the VCC3. Ranges from 3.135 to 3.465V

VCCRTC

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the voltage of the VCCRTC. Ranges from 3.135 to 3.465V

V_3P3_A

Type	Information
Found on BIOS Page	Advanced Page > Hardware Monitor
Description	Displays the voltage of the V_3P3_A. Ranges from 3.135 to 3.465V

[Advanced Page > S5 RTC Wake Settings](#)

Wake system from S5

Type	Configurable Setting
Found on BIOS Page	Advanced Page > S5 RTC Wake Settings
Description	Enable or disable system wake on alarm event. Possible values: Fixed Time: system will wake on the hr:min:sec specified. Dynamic Time: system will wake on the current time + increase minute(s)

[Advanced Page > CPU Configuration](#)

Socket 0 CPU Information

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration
Description	Socket specific CPU information. Press Enter when selected to go into associated Sub-Menu

Advanced Page > CPU Configuration > Socket 0 CPU Information

CPU Signature

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the CPU signature

Microcode Patch

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Display the microcode patch

Max CPU Speed

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the maximum speed of the CPU

Min CPU Speed

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the minimum speed of the CPU

Processor Cores

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the number of cores in the processor

Intel HT Technology

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays if Intel HT Technology is supported or not

Intel VT-x Technology

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays if Intel VT-x Technology is supported or not

VSM

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the voltage of the VSM

L1 Data Cache

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the L1 data cache size

L1 Code Cache

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the L1 code cache size

L2 Cache

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the L2 cache size

L3 Cache

Type	Information
Found on BIOS Page	Advanced Page > CPU Configuration > Socket 0 CPU Information
Description	Displays the L3 cache size

[Advanced Page > CPU Configuration > CPU Power Management](#)

EIST

Type	Configurable Setting
Found on BIOS Page	Advanced Page > CPU Configuration > CPU Power Management
Description	Enable/Disable Intel SpeedStep. Default value of Enabled

Turbo Mode

Type	Configurable Setting
Found on BIOS Page	Advanced Page > CPU Configuration > CPU Power Management
Description	Enable/Disable Turbo Mode. Default value of Enabled

[Advanced Page > AMI Graphic Output Protocol Policy](#)

Output Select

Type	Configurable Setting
Found on BIOS Page	Advanced Page > AMI Graphic Output Protocol Policy
Description	Select which output interface to output to. Possible values are DP1 and DP2

Advanced Page > Network Stack Configuration

Network Stack

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Network Stack Configuration
Description	Enable/Disable UEFI Network Stack

Advanced Page > USB Configuration

USB Module Version

Type	Information
Found on BIOS Page	Advanced Page > USB Configuration
Description	Displays the USB module version

USB Controllers

Type	Information
Found on BIOS Page	Advanced Page > USB Configuration
Description	Displays the USB controller number

USB Devices

Type	Information
Found on BIOS Page	Advanced Page > USB Configuration
Description	Displays the USB device number

Advanced Page > Platform Trust Technology (PTT)

fTPM

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Platform Trust Technology (PTT)
Description	Enable to activate fTPM. Disable to activate dTPM

Advanced Page > Thermal

Automatic Thermal Reporting

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	Configure _CRT, _PSV and _AC0 automatically based on values recommended in BWG's Thermal Reporting for Thermal Management settings. Set to Disabled for manual configuration

DPTF

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	Enable/Disable DPTF

DPTF Processor

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	Enable/Disable Processor Participant Device

Active Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Active Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 90, possible values: 0 to 127

Passive Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Passive Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 100, possible values: 0 to 127

S3/CS Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Critical Thermal Trip Point for entering S3 or CS. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 110, possible values: 0 to 127

Hot Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Hot Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 105, possible values: 0 to 127

Critical Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 105, possible values: 0 to 127

Thermal Sampling Period

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	The polling interval in 10ths of seconds. A value of 0 tells the driver to use interrupts. NOTE: the granularity of the sampling period is .1 seconds. For example, if the sampling period is 30 seconds, then _TSP needs to report 300; if the sampling period is .5 seconds, then choose 5. Default value of 0, possible values: 0 to 100

FAN Device

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	Enable/Disable the Fan device

Generic Device 1

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	Enable/Disable Thermistor 1 device

Activate Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Active Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 60, possible values of 0 to 127

Passive Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Passive Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 65, possible values of 0 to 127

S3/CS Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 70, possible values of 0 to 127

Hot Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Hot Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 75, possible values of 0 to 127

Critical Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 80, possible values of 0 to 127

Thermal Sampling Period

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	The polling interval in 10ths of seconds. A value of 0 tells the driver to use interrupts. NOTE: the granularity of the sampling period is .1 seconds. For example, if the sampling period is 30 seconds, then _TSP needs to report 300; if the sampling period is .5 seconds, then choose 5. Default value of 50, possible values: 0 to 100

Generic Device 2

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	Enable/Disable Thermistor 2 device

Active Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Active Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 60, possible values: 0 to 127

Passive Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Passive Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 65, possible values: 0 to 127

S3/CS Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Critical Thermal Trip Point for entering S3 or CS. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 70, possible values: 0 to 127

Hot Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Hot Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 75, possible values: 0 to 127

Critical Thermal Trip Point

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 80, possible values: 0 to 127

Thermal Sampling Period

Type	Configurable Setting
Found on BIOS Page	Advanced Page > Thermal
Description	The polling interval in 10ths of seconds. A value of 0 tells the driver to use interrupts. NOTE: the granularity of the sampling period is .1 seconds. For example, if the sampling period is 30 seconds, then _TSP needs to report 300; if the sampling period is .5 seconds, then choose 5. Default value of 50, possible values: 0 to 100

Advanced Page > System Component

PNP Setting

Type	Configurable Setting
Found on BIOS Page	Advanced Page > System Component
Description	Select PNP setting mode, Disable, Performance, Power or Power & Performance mode

Advanced Page > RC ACPI Settings

Native ASPM

Type	Configurable Setting
Found on BIOS Page	Advanced Page > RC ACPI Settings
Description	Enable/Disable Native ASPM. On Enable, vista will control the ASPM support for the device. If disabled, the BIOS will.

Low Power S0 Idle Capability

Type	Configurable Setting
Found on BIOS Page	Advanced Page > RC ACPI Settings
Description	This variable determines if we enable ACPI Low power S0 Idle Capability (mutually exclusive with Smart Connect). Also updates the Platform S0ix Capability Support in IGD OpRegion

Chipset Page

Total Memory

Type	Information
Found on BIOS Page	Chipset Page
Description	Displays the total memory size and type

On-Board Memory

Type	Information
Found on BIOS Page	Chipset Page
Description	Displays the total on-board memory size and type

Chipset Page > PCI Express Configuration

PCI Express Root Port 1

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration
Description	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

J_M2_KM

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration
Description	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

J_M2_KE

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration
Description	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

J_WLAN

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration
Description	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

[Chipset Page > PCI Express Configuration > PCI Express Root Port 1](#)

PCI Express Root Port 1

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > PCI Express Root Port 1
Description	Enable/Disable the PCI Express Root Port

ASPM

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > PCI Express Root Port 1
Description	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

[Chipset Page > PCI Express Configuration > J_M2_KM](#)

J_M2_KM

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > J_M2_KM
Description	Enable/Disable the PCI Express Root Port

ASPM

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > J_M2_KM
Description	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

[Chipset Page > PCI Express Configuration > J_M2_KE](#)

J_M2_KE

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > J_M2_KE
Description	Enable/Disable the PCI Express Root Port

ASPM

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > J_M2_KE
Description	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

[Chipset Page > PCI Express Configuration > J_WLAN](#)

J_WLAN

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > J_WLAN
Description	Enable/Disable the PCI Express Root Port

ASPM

Type	Configurable Setting
Found on BIOS Page	Chipset Page > PCI Express Configuration > J_WLAN
Description	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

Chipset Page > USB Configuration

USB VBUS

Type	Configurable Setting
Found on BIOS Page	Chipset Page > USB Configuration
Description	Enable/Disable USB VBUS. In HOST mode, VBUS should be Enable. VBUS should be Disable in OTG device mode

XHCI Compliance Mode

Type	Configurable Setting
Found on BIOS Page	Chipset Page > USB Configuration
Description	Options to Enable XHCI Link Compliance Mode. Default is Enable to enable Compliance Mode. Set Disable to disable Compliance Mode

Security Page

Setup Administrator Password

Type	Configurable Setting
Found on BIOS Page	Security Page
Description	Setup administrator password. Press Enter when selected to go into the associated Sub-Menu

User Password

Type	Configurable Setting
Found on BIOS Page	Security Page
Description	Setup user password. Press Enter when selected to go into the associated Sub-Menu

P1:TS128GMSA370

Type	Configurable Setting
Found on BIOS Page	Security Page
Description	HDD Security Configuration for selected drive. Press Enter when selected to go into the associated Sub-Menu

Security Page > Secure Boot

Secure Boot

Type	Configurable Setting
Found on BIOS Page	Security Page
Description	Customizable Secure Boot settings. Press Enter when selected to go into the associated Sub-Menu

Secure Boot

Type	Configurable Setting
Found on BIOS Page	Security Page > Secure Boot
Description	Enable/Disable Secure Boot. Secure Boot is activated when Platform Key (PK) is enrolled, System Mode is User/Deployed, and CSM function is disabled

Secure Boot Customization

Type	Configurable Setting
Found on BIOS Page	Security Page > Secure Boot
Description	Secure Boot Mode - Custom/Standard. Set UEFI Secure Boot Mode to STANDARD mode or CUSTOM mode, this change is effect after save. And after reset, the mode will return to STANDARD mode

Boot Page

Setup Prompt Timeout

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Number of seconds to wait for setup activation key. The prompt 65535(0xFFFF) means indefinite waiting. The default value is 3, and the possible values range from 1-65535

Bootup NumLock State

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Select the keyboard NumLock state. The default value is On, and the possible values are On/Off

Fast Boot

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Enable/Disable FastBoot features. Most probes are skipped to reduce time cost during boot

Boot Option #1

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is Hard Disk: Windows Boot Manager (P1:TS128MSA370). The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

Boot Option #2

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is SD. The possible values are: SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

Boot Option #3

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is USB Hard Disk. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

Boot Option #4

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is USB CD/DVD. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

Boot Option #5

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is USB Key. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

Boot Option #6

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is USB Floppy. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

Boot Option #7

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Sets the system boot order. Default Value is Network. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

UEFI Hard Disk Drive BBS Priorities

Type	Configurable Setting
Found on BIOS Page	Boot Page
Description	Specifies the Boot Device Priority sequence from available UEFI Hard Disk Drives. Press Enter when selected to go into the associated Sub-Menu

Boot Page > UEFI Hard Disk Drive BBS Priorities

Boot Option #1

Type	Configurable Setting
Found on BIOS Page	Boot Page > UEFI Hard Disk Drive BBS Priorities
Description	Sets the system boot order. The default value is Windows Boot Manager (P1:TS128MSA370), and the possible values are: Windows Boot Manager (P1:TS128MSA370), and Disable

BIOS Updates

The latest BIOS Updates are available [from the OnLogic support site.](#)