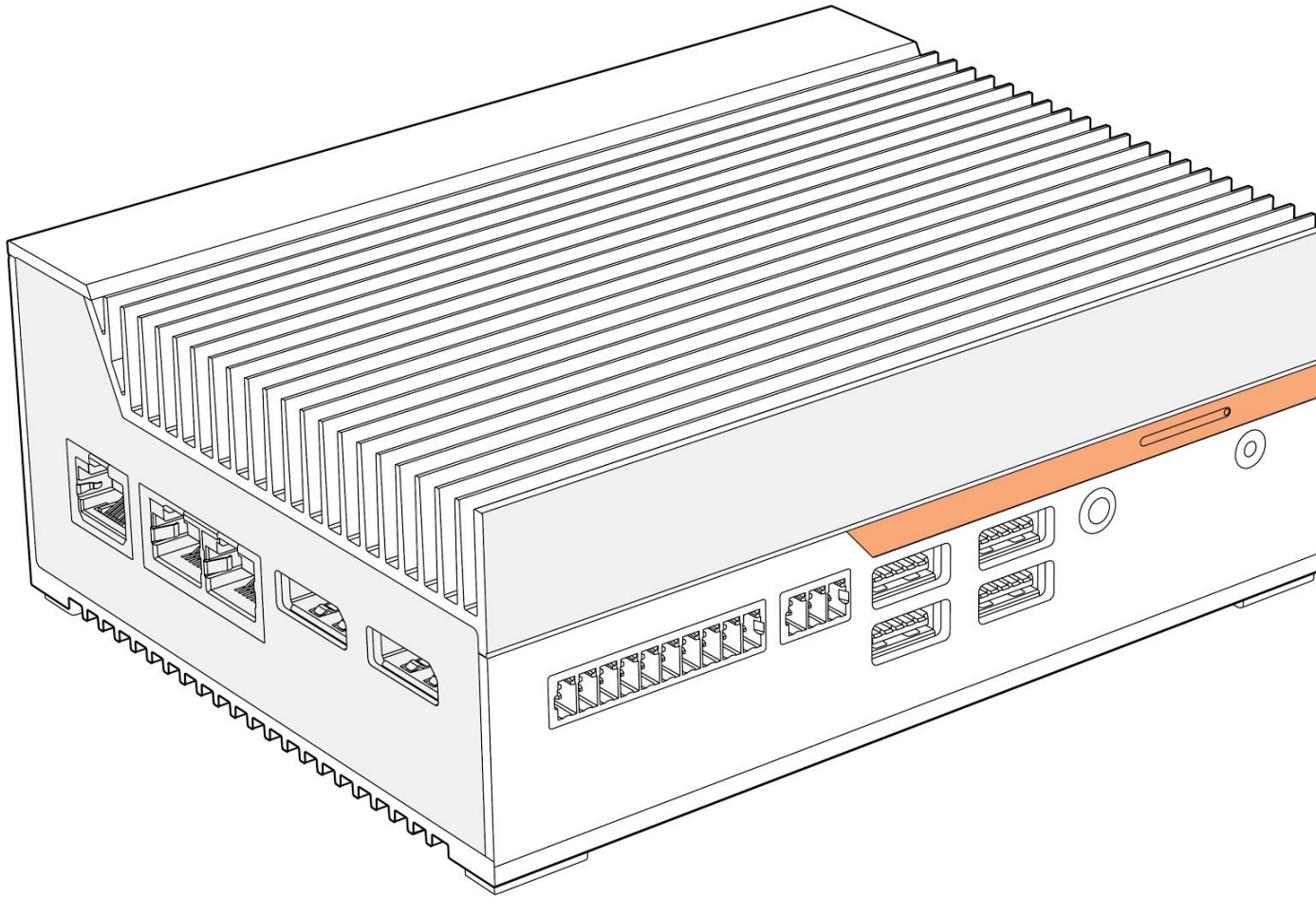


# Karbon 300 BIOS Manual



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## Main Page

### BIOS Vendor

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays BIOS vendor

### Core Version

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays current core version

### Compliancy

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the BIOS compliancy

### BIOS Version

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the current BIOS version

### Build Date

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the current build date in MM/DD/YYYY

### BXT SOC

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the SOC version

MRC Version

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the MRC version

PUNIT FW

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the PUNIT FW version

PMC FW

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the PMC FW version

TXE FW

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the TXE FW version

ISH FW

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the ISH FW version

GOP

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the GOP version

#### CPU Flavor

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the CPU flavor

#### Board ID

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the Board ID

#### Fab ID

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the Fab ID

#### Total Memory

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays the installed memory size

#### Memory Speed

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Displays clock speed of memory

#### System Language

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Select the current default language

### System Date

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Display the date in MM:DD:YYYY. Valid range is from 1 to 12, 1 to 31, 2005 to 2099. Use Tab to switch between date elements

### System Time

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Main Page
<b>Description</b>	Display the time in HH:MM:SS. Valid range is from 0 to 23, 0 to 59, 0 to 59. Use Tab to switch between time elements

## Advanced Page

### Watchdog Timer

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page
<b>Description</b>	Disabled: disable TCO watchdog timer, halt timer count: no reset will occur. Enabled: enable TCO watchdog timer, start timer count

### BIOS Timer

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page
<b>Description</b>	Set BIOS watchdog timer. Default value of 60. Possible values from 30 - 255

## Advanced Page > Driver Health

Press Enter to view the sub-sections for Driver Health

Intel(R) PRO /1000 7.0.06 PCI-E

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Driver Health
<b>Description</b>	Provides the health status for the drivers/controllers. Press Enter when selected to go into the associated Sub-Menu

## Advanced Page > NCT5524D Super IO Configuration

Super IO Chip

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > NCT5524D Super IO Configuration
<b>Description</b>	Displays the Super IO Chip type

Serial Port 1 & Serial Port 2

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > NCT5524D Super IO Configuration
<b>Description</b>	Enables or Disables serial port (COM). Use +/- to change option

Serial Port Mode

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > NCT5524D Super IO Configuration
<b>Description</b>	Displays the current serial port mode. Possible values are: 1T/1R RS-422, 3T/5R RS-232, 1T/1R RS-485 TX ENABLE Low Active, 1T/1R RS-485 with termination resistor TX ENABLE Low Active, 1T/1R RS-422 with termination resistor, Disabled

## Advanced Page > Hardware Monitor

### VR Temperature

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the temperature of the VR. Ranges from -40 to 105°C

### Memory Temperature

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the temperature of the memory. Ranges from -40 to 105°C

### VCORE

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the voltage of the VCORE. Ranges from .5 to 1.5V

### 3VSB

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the voltage of the 3VSB. Ranges from 3.135 to 3.465V

### VSM

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the voltage of the VSM. Ranges from 1.14 to 1.26V

### VCC3

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the voltage of the VCC3. Ranges from 3.135 to 3.465V



#### VCCRTC

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the voltage of the VCCRTC. Ranges from 3.135 to 3.465V

#### V\_3P3\_A

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > Hardware Monitor
<b>Description</b>	Displays the voltage of the V_3P3_A. Ranges from 3.135 to 3.465V

### [Advanced Page > S5 RTC Wake Settings](#)

#### Wake system from S5

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > S5 RTC Wake Settings
<b>Description</b>	Enable or disable system wake on alarm event. Possible values: Fixed Time: system will wake on the hr:min:sec specified. Dynamic Time: system will wake on the current time + increase minute(s)

### [Advanced Page > CPU Configuration](#)

#### Socket 0 CPU Information

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration
<b>Description</b>	Socket specific CPU information. Press Enter when selected to go into associated Sub-Menu

## Advanced Page > CPU Configuration > Socket 0 CPU Information

### CPU Signature

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the CPU signature

### Microcode Patch

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Display the microcode patch

### Max CPU Speed

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the maximum speed of the CPU

### Min CPU Speed

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the minimum speed of the CPU

### Processor Cores

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the number of cores in the processor

#### Intel HT Technology

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays if Intel HT Technology is supported or not

#### Intel VT-x Technology

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays if Intel VT-x Technology is supported or not

#### VSM

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the voltage of the VSM

#### L1 Data Cache

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the L1 data cache size

#### L1 Code Cache

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the L1 code cache size

#### L2 Cache

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the L2 cache size

### L3 Cache

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > Socket 0 CPU Information
<b>Description</b>	Displays the L3 cache size

### [Advanced Page > CPU Configuration > CPU Power Management](#)

### EIST

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > CPU Power Management
<b>Description</b>	Enable/Disable Intel SpeedStep. Default value of Enabled

### Turbo Mode

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > CPU Configuration > CPU Power Management
<b>Description</b>	Enable/Disable Turbo Mode. Default value of Enabled

### [Advanced Page > AMI Graphic Output Protocol Policy](#)

### Output Select

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > AMI Graphic Output Protocol Policy
<b>Description</b>	Select which output interface to output to. Possible values are DP1 and DP2

## Advanced Page > Network Stack Configuration

### Network Stack

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Network Stack Configuration
<b>Description</b>	Enable/Disable UEFI Network Stack

## Advanced Page > USB Configuration

### USB Module Version

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > USB Configuration
<b>Description</b>	Displays the USB module version

### USB Controllers

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > USB Configuration
<b>Description</b>	Displays the USB controller number

### USB Devices

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Advanced Page > USB Configuration
<b>Description</b>	Displays the USB device number

## Advanced Page > Platform Trust Technology (PTT)

### fTPM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Platform Trust Technology (PTT)
<b>Description</b>	Enable to activate fTPM. Disable to activate dTPM

## Advanced Page > Thermal

### Automatic Thermal Reporting

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	Configure _CRT, _PSV and _AC0 automatically based on values recommended in BWG's Thermal Reporting for Thermal Management settings. Set to Disabled for manual configuration

### DPTF

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	Enable/Disable DPTF

### DPTF Processor

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	Enable/Disable Processor Participant Device

#### Active Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Active Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 90, possible values: 0 to 127

#### Passive Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Passive Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 100, possible values: 0 to 127

#### S3/CS Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Critical Thermal Trip Point for entering S3 or CS. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 110, possible values: 0 to 127

#### Hot Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Hot Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 105, possible values: 0 to 127

#### Critical Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 105, possible values: 0 to 127

#### Thermal Sampling Period

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	The polling interval in 10ths of seconds. A value of 0 tells the driver to use interrupts. NOTE: the granularity of the sampling period is .1 seconds. For example, if the sampling period is 30 seconds, then _TSP needs to report 300; if the sampling period is .5 seconds, then choose 5. Default value of 0, possible values: 0 to 100

#### FAN Device

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	Enable/Disable the Fan device

#### Generic Device 1

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	Enable/Disable Thermistor 1 device

#### Activate Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Active Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 60, possible values of 0 to 127



### Passive Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Passive Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 65, possible values of 0 to 127

### S3/CS Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 70, possible values of 0 to 127

### Hot Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Hot Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 75, possible values of 0 to 127

### Critical Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 80, possible values of 0 to 127

### Thermal Sampling Period

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	The polling interval in 10ths of seconds. A value of 0 tells the driver to use interrupts. NOTE: the granularity of the sampling period is .1 seconds. For example, if the sampling period is 30 seconds, then _TSP needs to report 300; if the sampling period is .5 seconds, then choose 5. Default value of 50, possible values: 0 to 100

### Generic Device 2

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	Enable/Disable Thermistor 2 device

### Active Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Active Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 60, possible values: 0 to 127

### Passive Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Passive Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 65, possible values: 0 to 127

### S3/CS Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Critical Thermal Trip Point for entering S3 or CS. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 70, possible values: 0 to 127

### Hot Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Hot Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 75, possible values: 0 to 127

### Critical Thermal Trip Point

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	This value controls the temperature of the ACPI Critical Thermal Trip Point. NOTE: a value of zero will cause the DPTF driver to disable the trip point. Default value of 80, possible values: 0 to 127

### Thermal Sampling Period

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > Thermal
<b>Description</b>	The polling interval in 10ths of seconds. A value of 0 tells the driver to use interrupts. NOTE: the granularity of the sampling period is .1 seconds. For example, if the sampling period is 30 seconds, then _TSP needs to report 300; if the sampling period is .5 seconds, then choose 5. Default value of 50, possible values: 0 to 100

## Advanced Page > System Component

### PNP Setting

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > System Component
<b>Description</b>	Select PNP setting mode, Disable, Performance, Power or Power & Performance mode

## Advanced Page > RC ACPI Settings

### Native ASPM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > RC ACPI Settings
<b>Description</b>	Enable/Disable Native ASPM. On Enable, vista will control the ASPM support for the device. If disabled, the BIOS will.

### Low Power S0 Idle Capability

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Advanced Page > RC ACPI Settings
<b>Description</b>	This variable determines if we enable ACPI Low power S0 Idle Capability (mutually exclusive with Smart Connect). Also updates the Platform S0ix Capability Support in IGD OpRegion

## Chipset Page

### Total Memory

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Chipset Page
<b>Description</b>	Displays the total memory size and type

### On-Board Memory

<b>Type</b>	Information
<b>Found on BIOS Page</b>	Chipset Page
<b>Description</b>	Displays the total on-board memory size and type

## Chipset Page > PCI Express Configuration

### PCI Express Root Port 1

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration
<b>Description</b>	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

### J\_M2\_KM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration
<b>Description</b>	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

### J\_M2\_KE

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration
<b>Description</b>	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

## J\_WLAN

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration
<b>Description</b>	Enable/Disable the PCI Express Root Port. Press Enter when selected to go into the associated Sub-Menu

### [Chipset Page > PCI Express Configuration > PCI Express Root Port 1](#)

## PCI Express Root Port 1

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > PCI Express Root Port 1
<b>Description</b>	Enable/Disable the PCI Express Root Port

## ASPM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > PCI Express Root Port 1
<b>Description</b>	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

### [Chipset Page > PCI Express Configuration > J\\_M2\\_KM](#)

## J\_M2\_KM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > J_M2_KM
<b>Description</b>	Enable/Disable the PCI Express Root Port

#### ASPM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > J_M2_KM
<b>Description</b>	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

#### [Chipset Page > PCI Express Configuration > J\\_M2\\_KE](#)

#### J\_M2\_KE

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > J_M2_KE
<b>Description</b>	Enable/Disable the PCI Express Root Port

#### ASPM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > J_M2_KE
<b>Description</b>	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

#### [Chipset Page > PCI Express Configuration > J\\_WLAN](#)

#### J\_WLAN

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > J_WLAN
<b>Description</b>	Enable/Disable the PCI Express Root Port

#### ASPM

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > PCI Express Configuration > J_WLAN
<b>Description</b>	PCI Express Active State Power Management settings. Possible values are: Disable, L0s, L1, L0sL1, Auto

## Chipset Page > USB Configuration

### USB VBUS

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > USB Configuration
<b>Description</b>	Enable/Disable USB VBUS. In HOST mode, VBUS should be Enable. VBUS should be Disable in OTG device mode

### XHCI Compliance Mode

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Chipset Page > USB Configuration
<b>Description</b>	Options to Enable XHCI Link Compliance Mode. Default is Enable to enable Compliance Mode. Set Disable to disable Compliance Mode

## Security Page

### Setup Administrator Password

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Security Page
<b>Description</b>	Setup administrator password. Press Enter when selected to go into the associated Sub-Menu

### User Password

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Security Page
<b>Description</b>	Setup user password. Press Enter when selected to go into the associated Sub-Menu



P1:TS128GMSA370

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Security Page
<b>Description</b>	HDD Security Configuration for selected drive. Press Enter when selected to go into the associated Sub-Menu

## Security Page > Secure Boot

### Secure Boot

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Security Page
<b>Description</b>	Customizable Secure Boot settings. Press Enter when selected to go into the associated Sub-Menu

### Secure Boot

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Security Page > Secure Boot
<b>Description</b>	Enable/Disable Secure Boot. Secure Boot is activated when Platform Key (PK) is enrolled, System Mode is User/Deployed, and CSM function is disabled

### Secure Boot Customization

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Security Page > Secure Boot
<b>Description</b>	Secure Boot Mode - Custom/Standard. Set UEFI Secure Boot Mode to STANDARD mode or CUSTOM mode, this change is effect after save. And after reset, the mode will return to STANDARD mode

## Boot Page

### Setup Prompt Timeout

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Number of seconds to wait for setup activation key. The prompt 65535(0xFFFF) means indefinite waiting. The default value is 3, and the possible values range from 1-65535

### Bootup NumLock State

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Select the keyboard NumLock state. The default value is On, and the possible values are On/Off

### Fast Boot

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Enable/Disable FastBoot features. Most probes are skipped to reduce time cost during boot

### Boot Option #1

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is Hard Disk: Windows Boot Manager (P1:TS128MSA370). The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### Boot Option #2

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is SD. The possible values are: SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### Boot Option #3

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is USB Hard Disk. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### Boot Option #4

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is USB CD/DVD. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### Boot Option #5

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is USB Key. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### Boot Option #6

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is USB Floppy. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### Boot Option #7

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Sets the system boot order. Default Value is Network. The possible values are: Hard Disk: Windows Boot Manager (P1:TS128MSA370), SD, USB Hard Disk, USB CD/DVD, USB Key, USB Floppy, Network, and Disabled

#### UEFI Hard Disk Drive BBS Priorities

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page
<b>Description</b>	Specifies the Boot Device Priority sequence from available UEFI Hard Disk Drives. Press Enter when selected to go into the associated Sub-Menu

### [Boot Page > UEFI Hard Disk Drive BBS Priorities](#)

#### Boot Option #1

<b>Type</b>	Configurable Setting
<b>Found on BIOS Page</b>	Boot Page > UEFI Hard Disk Drive BBS Priorities
<b>Description</b>	Sets the system boot order. The default value is Windows Boot Manager (P1:TS128MSA370), and the possible values are: Windows Boot Manager (P1:TS128MSA370), and Disable